

IN THE SPECIFICATION:

Amend the specification as follows:

page 1, line 6:

--keeping the results of all parallel operations currently being executed. The usage of a--

page 1, lines 16:

-- ~~restrains~~ **restraints** rather than computation restrains. One solution to this problem is to portion--

page 1, line 22:

-- Sun MAJC, Equator MAP-CA, BOPS ManArray etc. In a clustered processor, resources [7] like--

page 3, line 33:

-- pipeline registers can be arranged in said control connections depending on the distance--

page 5, line 17:

-- Instruction 2 comprises op2 and **op5** ~~op4~~, which are executed in--